Abstract of the Disclosure

The invention relates to a method for performing a memory processing function to a block memory (2), wherein there are memory cells (18), where data can be stored, and a connection bus (5), wherein there is at least a ready/busy line (5h). The ready/busy line (5h) can be set at least to a ready status and a busy status. In the method, setting the status of said ready/busy line (5h) to said busy status is performed at the beginning of the processing function, and the detection of processing errors is performed. The end of processing function is indicated by setting said ready/busy line (5h) to said ready status. After detecting a processing error, the status of said ready/busy line (5h) is changed back to said busy status if an processing error is detected. The invention also relates to a system, an electronic device (1) and a block memory (2), in which the method is applied.